

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Robert Kenneth Leidy, et al.

Examiner: Unassigned

Serial No: To be assigned

Art Unit: Unassigned

Filed: Herewith

Docket: BUR920010114US1 (14873)

For: METHOD AND SYSTEM FOR
DETERMINING OVERLAY TOLERANCE

Dated: November 15, 2001

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 4,399,205, issued August 16, 1983;
2. U.S. Patent No. 5,124,927, issued June 23, 1992;
3. U.S. Patent No. 5,633,714, issued May 27, 1997;

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

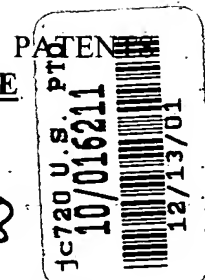
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Dated: 12/13/2001

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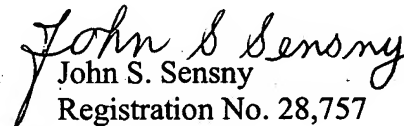


4. U.S. Patent No. 5,701,013, issued December 23, 1997;
5. U.S. Patent No. 5,712,707, issued January 27, 1998;
6. U.S. Patent No. 6,130,750, issued October 10, 2000;
7. U.S. Patent No. 6,165,656, issued December 26, 2000;
8. U.S. Patent No. 6,218,200, issued April 17, 2001;
9. U.S. Patent No. 6,242,757, issued June 5, 2001; and
10. U.S. Patent No. 6,251,745, issued June 26, 2001

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,


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